

CONTENT	SHEET
Cover Sheet, Block diagram	1-2
K9 M2 CPU-Signals/Memorys/Power/GND	3-5
FIRST LOGICAL DDR DIMM	6
SECOND LOGIC DDR DIMM	7
DDR Termination & EMI	8
MCP65P HT&PEX X16	9
MCP65P PEX X1 & GPHY	10
MCP65P PCI & LPC & SATA & IDE	11
MCP65P HDA & USB & GPIO & MSIC	12
MCP65P POWER & GND	13
Pri PCI-E Slot 8X & 1X	14
Sec PCI-E Slot 8X & 1X	15
PCI Slot 1 & 2	16
FINTEK 71882	17
Audio Codec ALC888	18
Realtek 8211BL	19
USB	20
ACPI UPI	21
DDR Power & Chip Power	22
ATX&Front&FAN&IDE&SPI&PARALL	23
VRM	24
Manual Parts	25
Power Map	26
Power Sequence	27
Reset Signals	28
Clock Distribution	29
GPIO Configuration	30

MS-7390

ATX Version: 0A

CPU: AMD AM2& AM2+ Socket940
AM2 89W (3000+~6000+)
AM2+ up to 95W


System Chipset:
nVIDIA MCP65P(NF570LT-SLI)

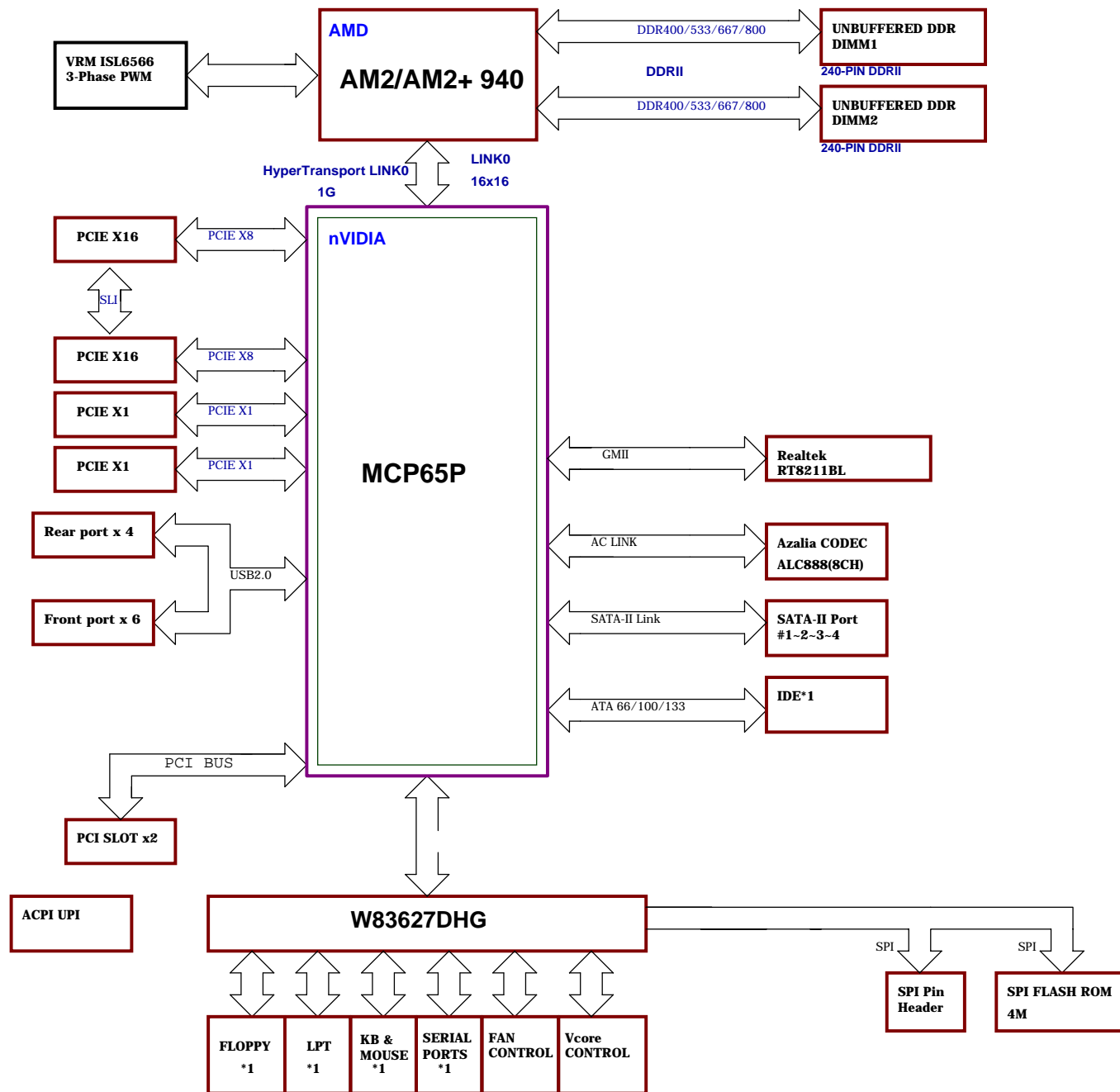
On Board Device:
LPC Super I/O -- Fintek F71882F
LAN -- Realtek 8211BL (PHY)
HD Audio Codec -- ALC888

Main Memory:
Dual-channel DDR-II *4

Expansion Slots:
PCI EXPRESS X16 SLOT *2(Real X8)-SLI
PCI EXPRESS X1 SLOT * 2
PCI SLOT * 2

PWM: Intersil ISL6566 (3Phases)

		MICRO-START INTL CO.,LTD.	
Title Cover Page			
Size Custom	Document Number MS-7390		Rev 0A
Date: Friday, November 30, 2007	Sheet 1	of 30	



6.7 MEM_MA_DQS_L[7..0] >> MEM_MA_DATA[63..0] 6.7
6.7 MEM_MA_DQS_H[7..0] >>
6.7 MEM_MA_DM[7..0] >>
6.7.8 MEM_MA_ADD[15..0] >>

6.7 MEM_MB_DQS_L[7..0] >> MEM_MB_DATA[63..0] 6.7
6.7 MEM_MB_DQS_H[7..0] >>
6.7 MEM_MB_DM[7..0] >>
6.7.8 MEM_MB_ADD[15..0] >>

CPU1B

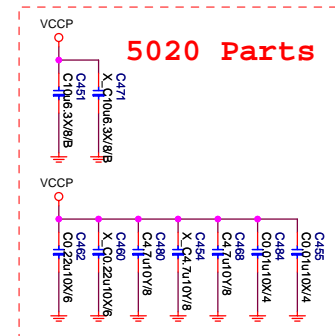
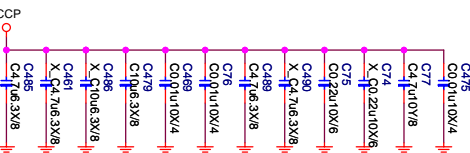
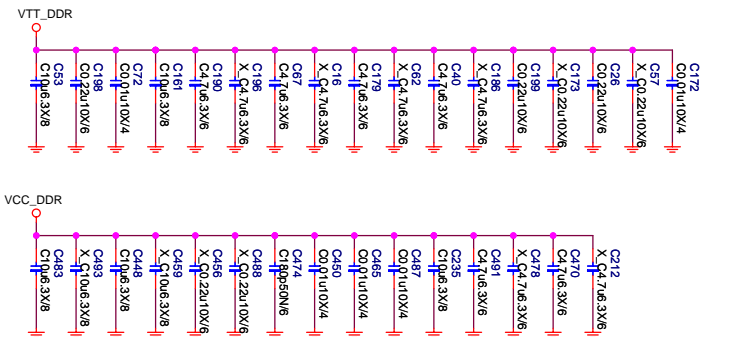
6.8 MEM_MA0_CLK_H2	>> MEM_MA0_CLK_H2	AG21	MA0_CLK_H(2)	MA_DATA(63)	AE14	MEM_MA_DATA63
6.8 MEM_MA0_CLK_L2	>> MEM_MA0_CLK_L2	AG20	MA0_CLK_L(2)	MA_DATA(62)	AG14	MEM_MA_DATA62
6.8 MEM_MA0_CLK_H1	>> MEM_MA0_CLK_H1	G19	MA0_CLK_H(1)	MA_DATA(61)	AG16	MEM_MA_DATA61
6.8 MEM_MA0_CLK_L1	>> MEM_MA0_CLK_L1	H19	MA0_CLK_L(1)	MA_DATA(60)	AD17	MEM_MA_DATA60
6.8 MEM_MA0_CLK_H0	>> MEM_MA0_CLK_H0	U27	MA0_CLK_H(0)	MA_DATA(59)	AD13	MEM_MA_DATA59
6.8 MEM_MA0_CLK_L0	>> MEM_MA0_CLK_L0	U26	MA0_CLK_L(0)	MA_DATA(58)	AE13	MEM_MA_DATA58
				MA_DATA(57)	AG15	MEM_MA_DATA57
6.8 MEM_MA0_CS_L1	>> MEM_MA0_CS_L1	AC25	MA0_CS_L(1)	MA_DATA(56)	AE16	MEM_MA_DATA56
6.8 MEM_MA0_CS_L0	>> MEM_MA0_CS_L0	AA24	MA0_CS_L(0)	MA_DATA(55)	AG17	MEM_MA_DATA55
				MA_DATA(54)	AE18	MEM_MA_DATA54
6.8 MEM_MA0_ODT0	>> MEM_MA0_ODT0	AC28	MA0_ODT(0)	MA_DATA(53)	AD21	MEM_MA_DATA53
				MA_DATA(52)	AG22	MEM_MA_DATA52
7.8 MEM_MA1_CLK_H2	>> MEM_MA1_CLK_H2	AE20	MA1_CLK_H(2)	MA_DATA(51)	AE17	MEM_MA_DATA51
7.8 MEM_MA1_CLK_L2	>> MEM_MA1_CLK_L2	AE19	MA1_CLK_L(2)	MA_DATA(50)	AE17	MEM_MA_DATA50
7.8 MEM_MA1_CLK_H1	>> MEM_MA1_CLK_H1	G20	MA1_CLK_H(1)	MA_DATA(49)	AE21	MEM_MA_DATA49
7.8 MEM_MA1_CLK_L1	>> MEM_MA1_CLK_L1	V27	MA1_CLK_L(1)	MA_DATA(48)	AE23	MEM_MA_DATA48
7.8 MEM_MA1_CLK_H0	>> MEM_MA1_CLK_H0	V27	MA1_CLK_H(0)	MA_DATA(47)	AE23	MEM_MA_DATA47
7.8 MEM_MA1_CLK_L0	>> MEM_MA1_CLK_L0	V27	MA1_CLK_L(0)	MA_DATA(46)	AJ26	MEM_MA_DATA46
				MA_DATA(45)	AG26	MEM_MA_DATA45
7.8 MEM_MA1_CS_L1	>> MEM_MA1_CS_L1	AD27	MA1_CS_L(1)	MA_DATA(44)	AE22	MEM_MA_DATA44
7.8 MEM_MA1_CS_L0	>> MEM_MA1_CS_L0	AA25	MA1_CS_L(0)	MA_DATA(43)	AG23	MEM_MA_DATA43
				MA_DATA(42)	AH25	MEM_MA_DATA42
7.8 MEM_MA1_ODT0	>> MEM_MA1_ODT0	AC27	MA1_ODT(0)	MA_DATA(41)	AE25	MEM_MA_DATA41
				MA_DATA(40)	AJ28	MEM_MA_DATA39
6.7.8 MEM_MA_CAS_L	>> MEM_MA_CAS_L	AB25	MA_CAS_L	MA_DATA(38)	AJ29	MEM_MA_DATA38
6.7.8 MEM_MA_WE_L	>> MEM_MA_WE_L	AB27	MA_WE_L	MA_DATA(37)	AE28	MEM_MA_DATA37
6.7.8 MEM_MA_RAS_L	>> MEM_MA_RAS_L	AA26	MA_RAS_L	MA_DATA(36)	AJ27	MEM_MA_DATA36
				MA_DATA(35)	AH27	MEM_MA_DATA35
6.7.8 MEM_MA_BANK2	>> MEM_MA_BANK2	N25	MA_BANK(2)	MA_DATA(34)	AG29	MEM_MA_DATA34
6.7.8 MEM_MA_BANK1	>> MEM_MA_BANK1	Y27	MA_BANK(1)	MA_DATA(33)	AE27	MEM_MA_DATA33
6.7.8 MEM_MA_BANK0	>> MEM_MA_BANK0	AA27	MA_BANK(0)	MA_DATA(32)	E28	MEM_MA_DATA31
				MA_DATA(31)	D27	MEM_MA_DATA30
7.8 MEM_MA_CKE1	>> MEM_MA_CKE1	L27	MA_CKE(1)	MA_DATA(29)	C27	MEM_MA_DATA28
6.8 MEM_MA_CKE0	>> MEM_MA_CKE0	M25	MA_CKE(0)	MA_DATA(28)	G26	MEM_MA_DATA27
				MA_DATA(27)	F27	MEM_MA_DATA26
				MA_DATA(26)	C28	MEM_MA_DATA25
				MA_DATA(25)	E27	MEM_MA_DATA24
				MA_DATA(24)	F25	MEM_MA_DATA23
				MA_DATA(23)	E25	MEM_MA_DATA22
				MA_DATA(22)	E23	MEM_MA_DATA21
				MA_DATA(21)	D23	MEM_MA_DATA20
				MA_DATA(20)	C26	MEM_MA_DATA19
				MA_DATA(19)	G23	MEM_MA_DATA18
				MA_DATA(18)	F23	MEM_MA_DATA17
				MA_DATA(16)	E22	MEM_MA_DATA15
				MA_DATA(15)	E21	MEM_MA_DATA14
				MA_DATA(14)	F17	MEM_MA_DATA13
				MA_DATA(13)	G17	MEM_MA_DATA12
				MA_DATA(12)	G22	MEM_MA_DATA11
				MA_DATA(11)	F21	MEM_MA_DATA10
				MA_DATA(10)	G18	MEM_MA_DATA9
				MA_DATA(9)	E17	MEM_MA_DATA8
				MA_DATA(8)	G16	MEM_MA_DATA7
				MA_DATA(7)	E15	MEM_MA_DATA6
				MA_DATA(6)	G13	MEM_MA_DATA5
				MA_DATA(5)	H13	MEM_MA_DATA4
				MA_DATA(4)	H17	MEM_MA_DATA3
				MA_DATA(3)	E16	MEM_MA_DATA2
				MA_DATA(2)	E14	MEM_MA_DATA1
				MA_DATA(1)	G14	MEM_MA_DATA0
				MA_DATA(0)		
				MA_DQS_H(8)	J28	X
				MA_DQS_L(8)	J27	X
				MA_DM(8)	J25	X
				MA_DM(7)	K25	X
				MA_CHECK(7)	J26	X
				MA_CHECK(6)	G28	X
				MA_CHECK(5)	G27	X
				MA_DM(4)	L24	X
				MA_CHECK(3)	K27	X
				MA_CHECK(2)	H29	X
				MA_DM(1)	H27	X
				MA_CHECK(0)		
				MA_DM(0)		

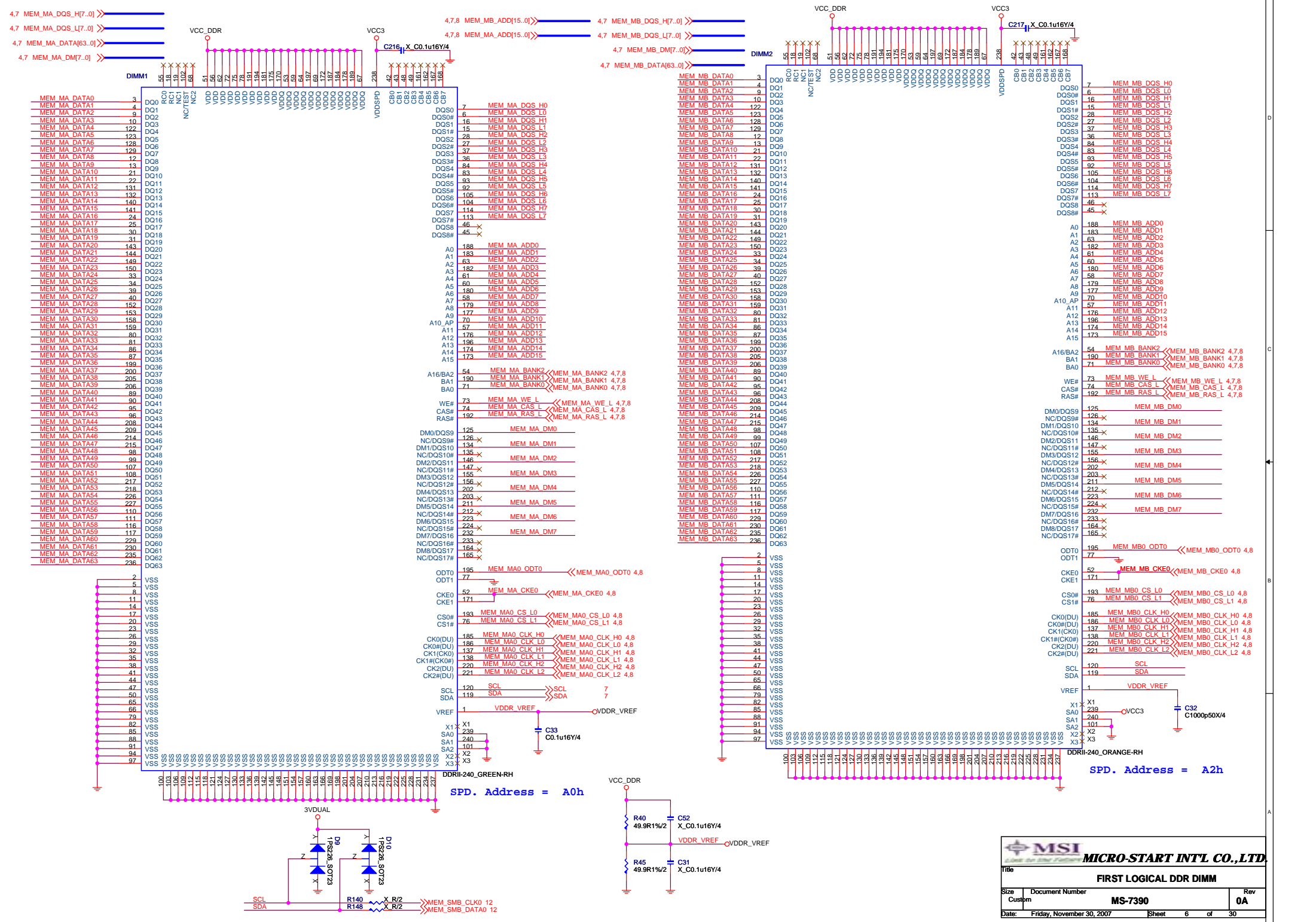
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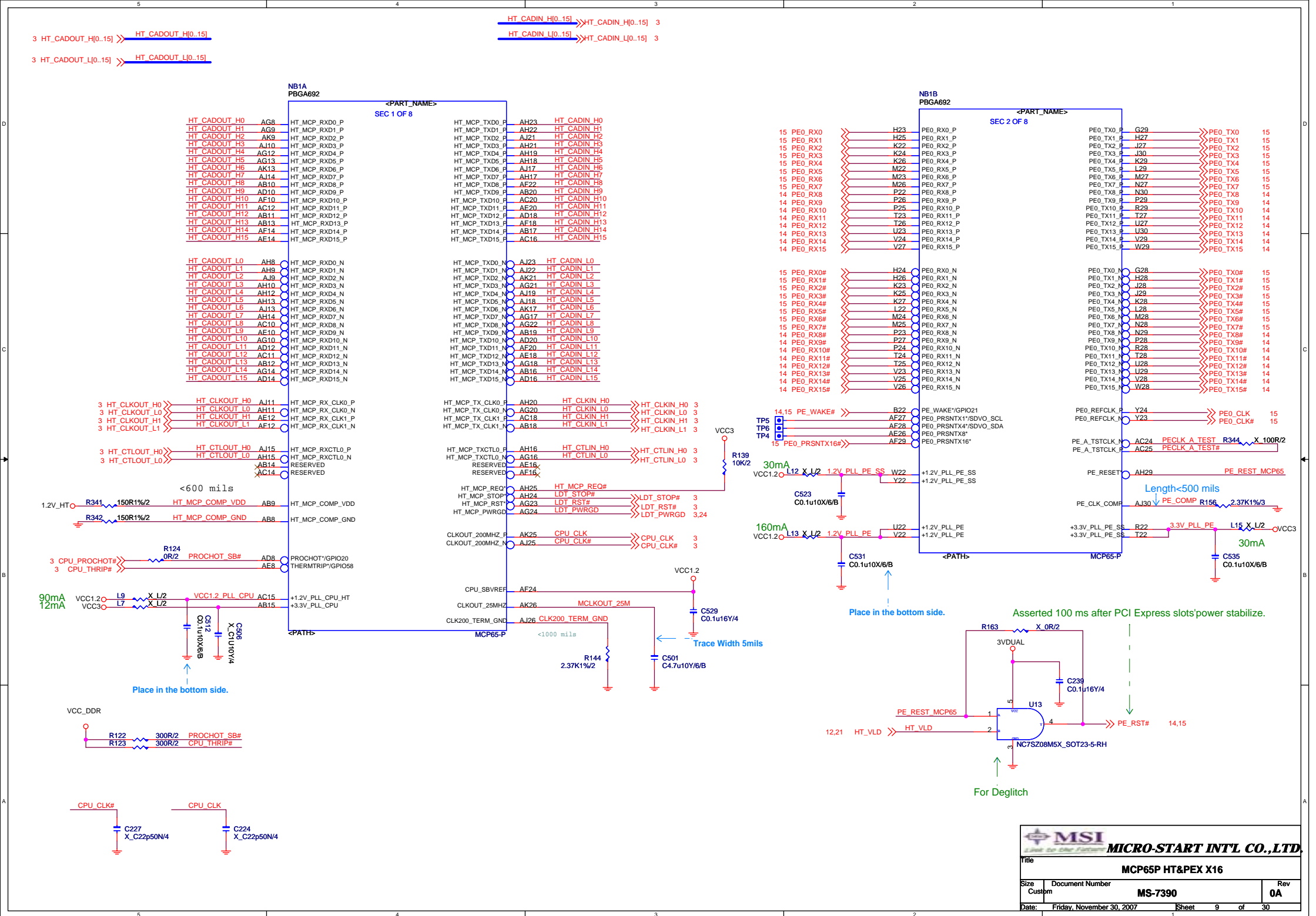
CPU1C

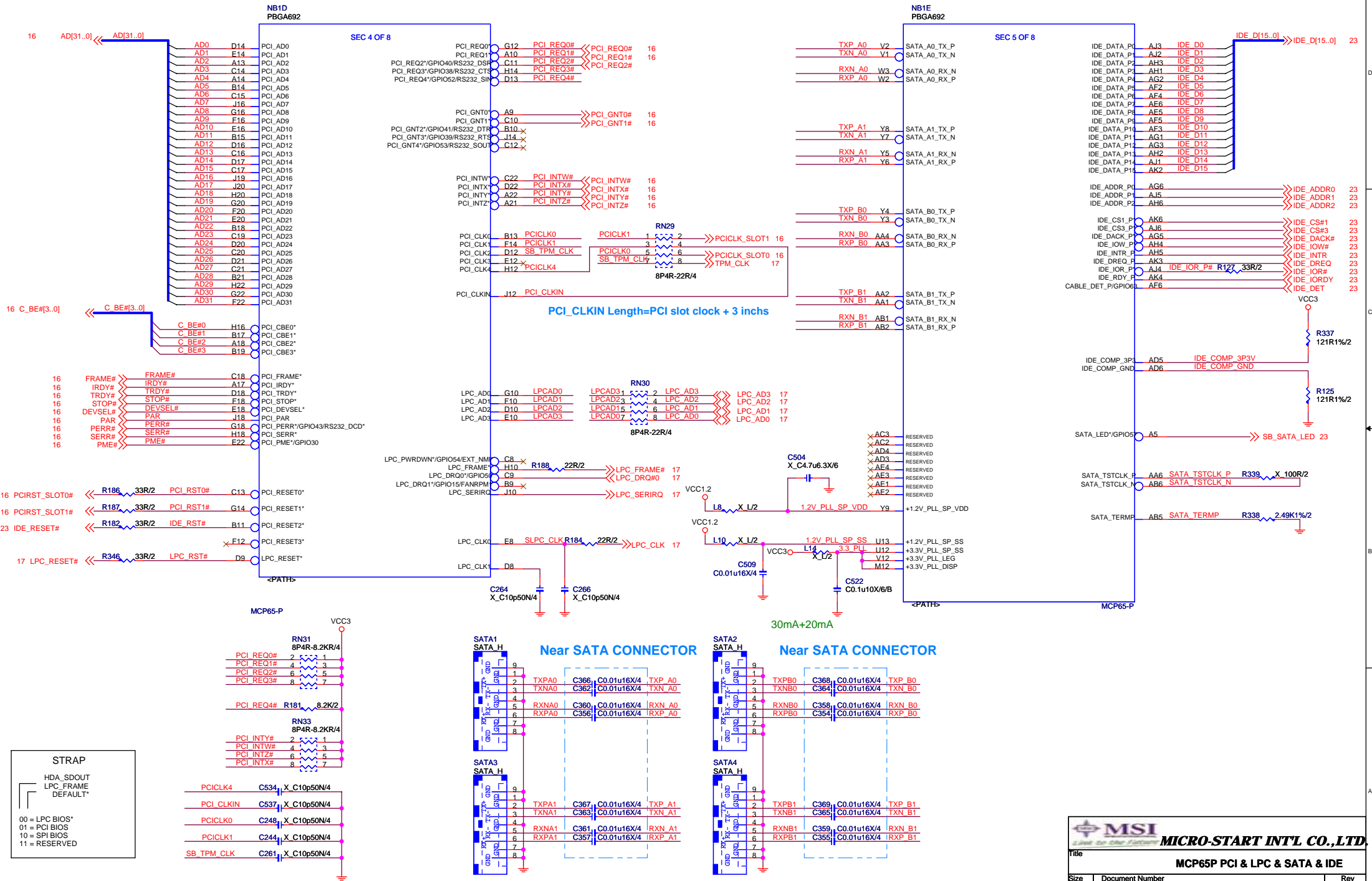
6.8 MEM_MB0_CLK_H2	>> MEM_MB0_CLK_H2	AJ19	MB0_CLK_H(2)	MB_DATA(63)	AH13	MEM_MB_DATA63
6.8 MEM_MB0_CLK_L2	>> MEM_MB0_CLK_L2	AK19	MB0_CLK_L(2)	MB_DATA(62)	AL13	MEM_MB_DATA62
6.8 MEM_MB0_CLK_H1	>> MEM_MB0_CLK_H1	A19	MB0_CLK_H(1)	MB_DATA(61)	AJ15	MEM_MB_DATA61
6.8 MEM_MB0_CLK_L1	>> MEM_MB0_CLK_L1	A19	MB0_CLK_L(1)	MB_DATA(60)	AL15	MEM_MB_DATA60
6.8 MEM_MB0_CLK_H0	>> MEM_MB0_CLK_H0	U31	MB0_CLK_H(0)	MB_DATA(59)	AE13	MEM_MB_DATA59
6.8 MEM_MB0_CLK_L0	>> MEM_MB0_CLK_L0	U30	MB0_CLK_L(0)	MB_DATA(58)	AG13	MEM_MB_DATA58
				MB_DATA(57)	AL14	MEM_MB_DATA57
6.8 MEM_MB0_CS_L1	>> MEM_MB0_CS_L1	AE30	MB0_CS_L(1)	MB_DATA(56)	AL16	MEM_MB_DATA56
6.8 MEM_MB0_CS_L0	>> MEM_MB0_CS_L0	AC31	MB0_CS_L(0)	MB_DATA(55)	AL17	MEM_MB_DATA55
				MB_DATA(54)	AK21	MEM_MB_DATA54
6.8 MEM_MB0_ODT0	>> MEM_MB0_ODT0	AD29	MB0_ODT(0)	MB_DATA(53)	AK21	MEM_MB_DATA53
				MB_DATA(52)	AL21	MEM_MB_DATA52
7.8 MEM_MB1_CLK_H2	>> MEM_MB1_CLK_H2	AL19	MB1_CLK_H(2)	MB_DATA(51)	AH15	MEM_MB_DATA51
7.8 MEM_MB1_CLK_L2	>> MEM_MB1_CLK_L2	AL18	MB1_CLK_L(2)	MB_DATA(50)	AL16	MEM_MB_DATA50
7.8 MEM_MB1_CLK_H1	>> MEM_MB1_CLK_H1	C19	MB1_CLK_H(1)	MB_DATA(49)	AH19	MEM_MB_DATA49
7.8 MEM_MB1_CLK_L1	>> MEM_MB1_CLK_L1	D19	MB1_CLK_L(1)	MB_DATA(48)	AL20	MEM_MB_DATA48
7.8 MEM_MB1_CLK_H0	>> MEM_MB1_CLK_H0	W29	MB1_CLK_H(0)	MB_DATA(47)	AJ22	MEM_MB_DATA47
7.8 MEM_MB1_CLK_L0	>> MEM_MB1_CLK_L0	W28	MB1_CLK_L(0)	MB_DATA(46)	AJ22	MEM_MB_DATA46
				MB_DATA(45)	AL24	MEM_MB_DATA45
7.8 MEM_MB1_CS_L1	>> MEM_MB1_CS_L1	AE29	MB1_CS_L(1)	MB_DATA(44)	AK25	MEM_MB_DATA44
7.8 MEM_MB1_CS_L0	>> MEM_MB1_CS_L0	AB31	MB1_CS_L(0)	MB_DATA(43)	AJ21	MEM_MB_DATA43
				MB_DATA(42)	AH21	MEM_MB_DATA42
7.8 MEM_MB1_ODT0	>> MEM_MB1_ODT0	AD31	MB1_ODT(0)	MB_DATA(41)	AH23	MEM_MB_DATA41
				MB_DATA(40)	AJ24	MEM_MB_DATA40
				MB_DATA(39)	AJ27	MEM_MB_DATA39
6.7.8 MEM_MB_CAS_L	>> MEM_MB_CAS_L	AC29	MB_CAS_L	MB_DATA(38)	AK27	MEM_MB_DATA38
6.7.8 MEM_MB_WE_L	>> MEM_MB_WE_L	AC30	MB_WE_L	MB_DATA(37)	AH31	MEM_MB_DATA37
6.7.8 MEM_MB_RAS_L	>> MEM_MB_RAS_L	AB29	MB_RAS_L	MB_DATA(36)	AG30	MEM_MB_DATA36
				MB_DATA(35)	AL25	MEM_MB_DATA35
6.7.8 MEM_MB_BANK2	>> MEM_MB_BANK2	N31	MB_BANK(2)	MB_DATA(34)	AJ26	MEM_MB_DATA34
6.7.8 MEM_MB_BANK1	>> MEM_MB_BANK1	AA31	MB_BANK(1)	MB_DATA(33)	AJ30	MEM_MB_DATA33
6.7.8 MEM_MB_BANK0	>> MEM_MB_BANK0	AA28	MB_BANK(0)	MB_DATA(32)	AJ31	MEM_MB_DATA32
				MB_DATA(31)	E31	MEM_MB_DATA31
7.8 MEM_MB_CKE1	>> MEM_MB_CKE1	M31	MB_CKE(1)	MB_DATA(29)	E30	MEM_MB_DATA30
6.8 MEM_MB_CKE0	>> MEM_MB_CKE0	M29	MB_CKE(0)	MB_DATA(28)	B27	MEM_MB_DATA29
				MB_DATA(27)	A27	MEM_MB_DATA28
				MB_DATA(26)	F29	MEM_MB_DATA27
				MB_DATA(25)	F31	MEM_MB_DATA26
				MB_DATA(24)	A29	MEM_MB_DATA25
				MB_DATA(23)	A28	MEM_MB_DATA24
				MB_DATA(22)	A25	MEM_MB_DATA23
				MB_DATA(21)	A24	MEM_MB_DATA22
				MB_DATA(20)	C22	MEM_MB_DATA21
				MB_DATA(19)	D21	MEM_MB_DATA20
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				MB_DATA(16)	B23	MEM_MB_DATA17
				MB_DATA(15)	A22	MEM_MB_DATA16
				MB_DATA(14)	B21	MEM_MB_DATA15
				MB_DATA(13)	C16	MEM_MB_DATA14
				MB_DATA(12)	D15	MEM_MB_DATA13
				MB_DATA(11)	C21	MEM_MB_DATA12
				MB_DATA(10)	A21	MEM_MB_DATA11
				MB_DATA(9)	A17	MEM_MB_DATA10
				MB_DATA(8)	B15	MEM_MB_DATA9
				MB_DATA(7)	B16	MEM_MB_DATA8
				MB_DATA(6)	A14	MEM_MB_DATA7
				MB_DATA(5)	E13	MEM_MB_DATA6
				MB_DATA(4)	F13	MEM_MB_DATA5
				MB_DATA(3)	C15	MEM_MB_DATA4
				MB_DATA(2)	A15	MEM_MB_DATA3
				MB_DATA(1)	D13	MEM_MB_DATA2
				MB_DATA(0)	D13	MEM_MB_DATA1
				MB_DQS_H(8)	J31	X
				MB_DQS_L(8)	J30	X
				MB_DM(8)	J29	X
				MB_DM(7)	K29	X
				MB_CHECK(7)	K31	X
				MB_CHECK(6)	G30	X
				MB_CHECK(5)	G30	X
				MB_CHECK(4)	G29	X
				MB_CHECK(3)	L29	X
				MB_CHECK(2)	H31	X
				MB_CHECK(1)	H31	X
				MB_CHECK(0)	G31	X

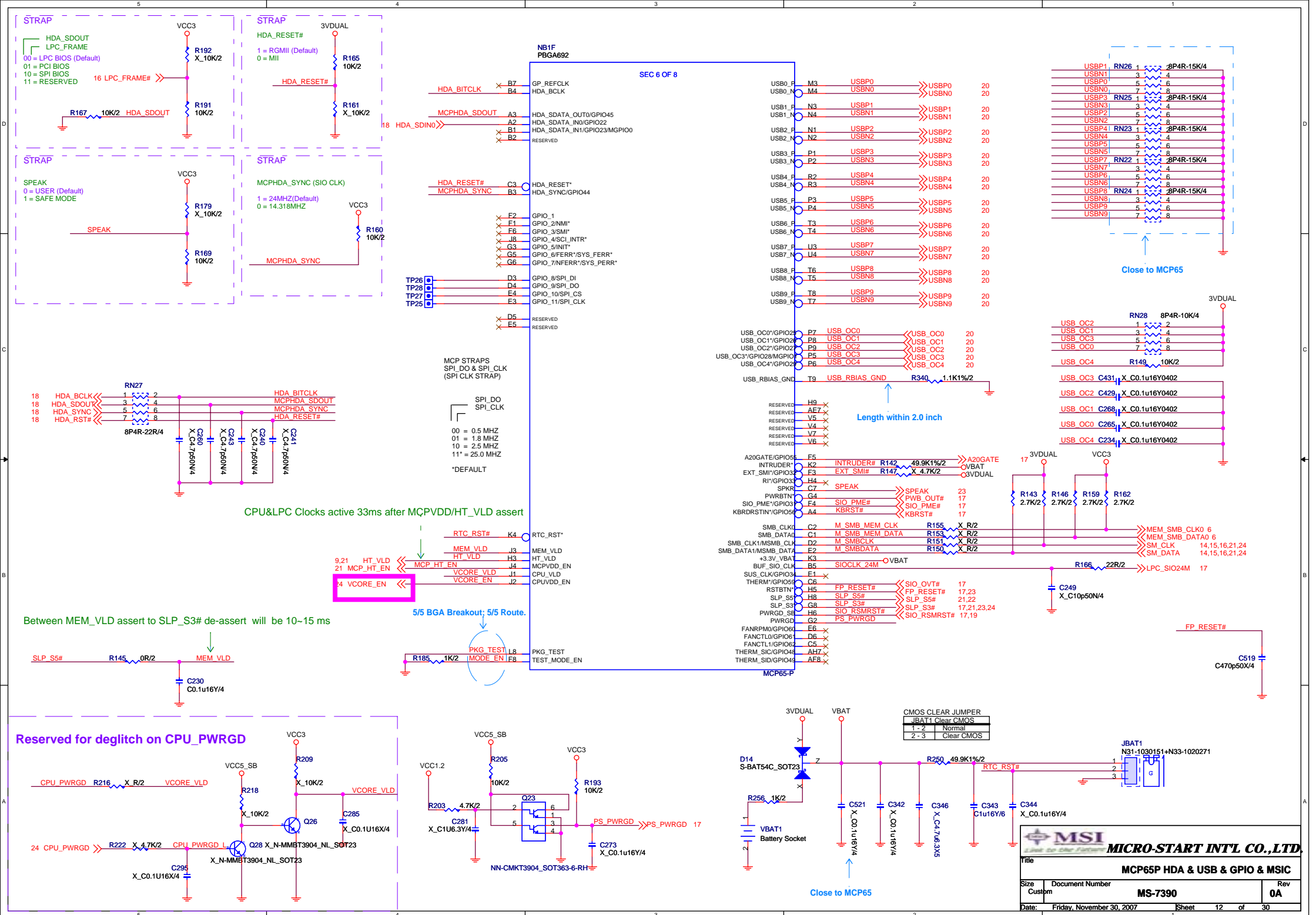
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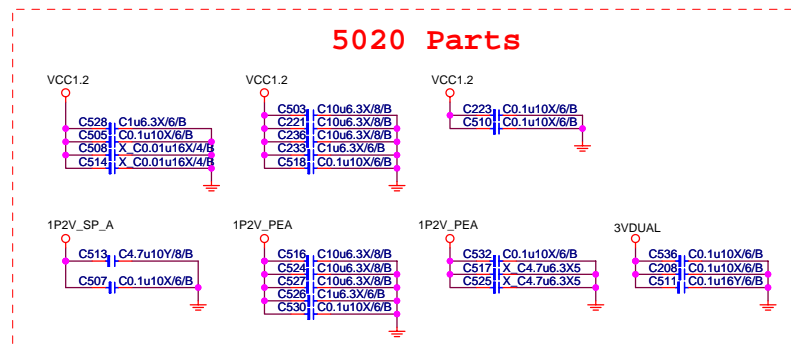
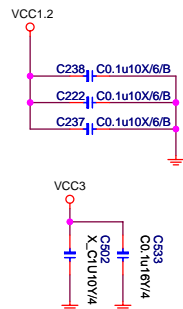
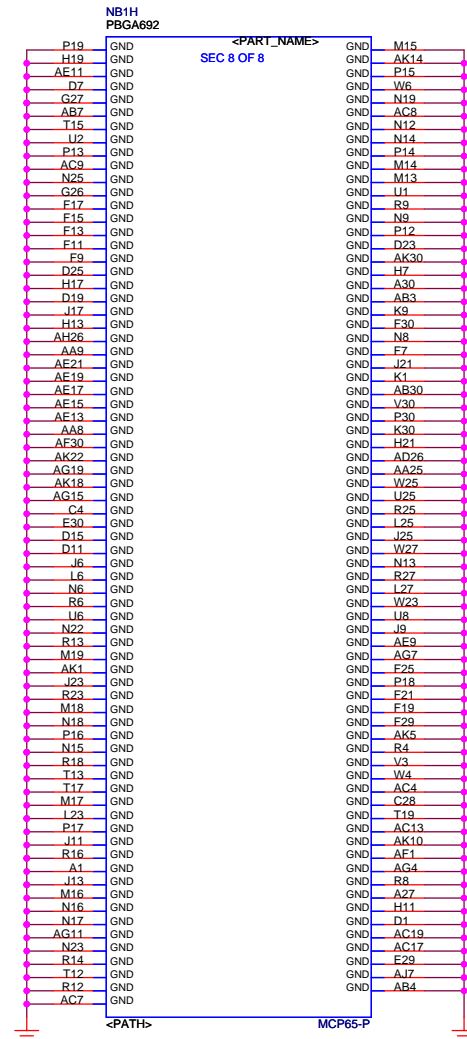
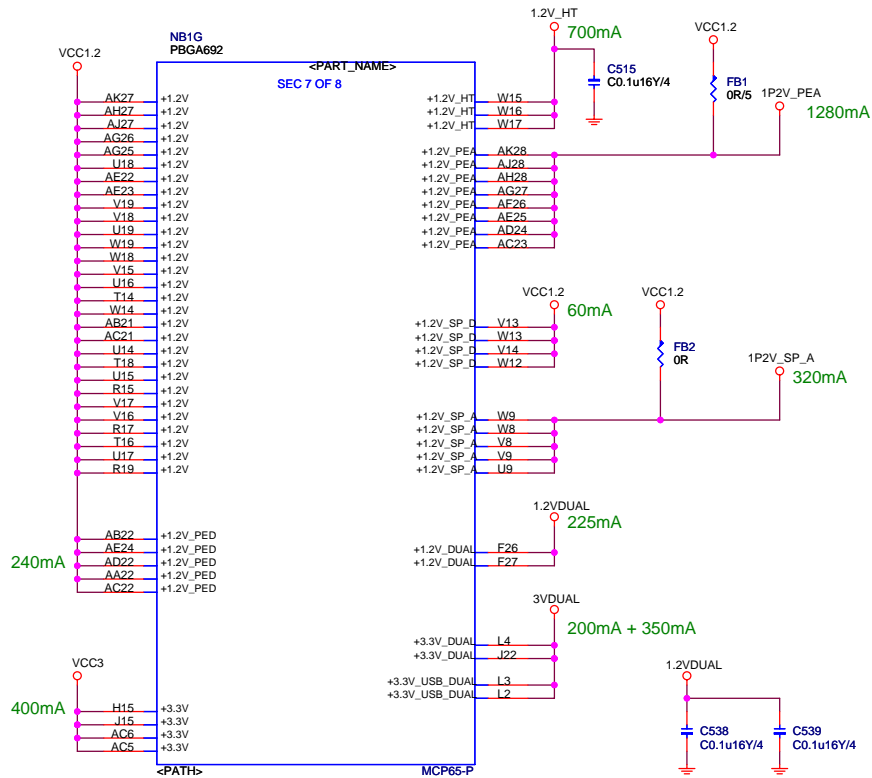





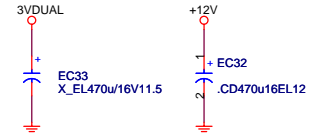
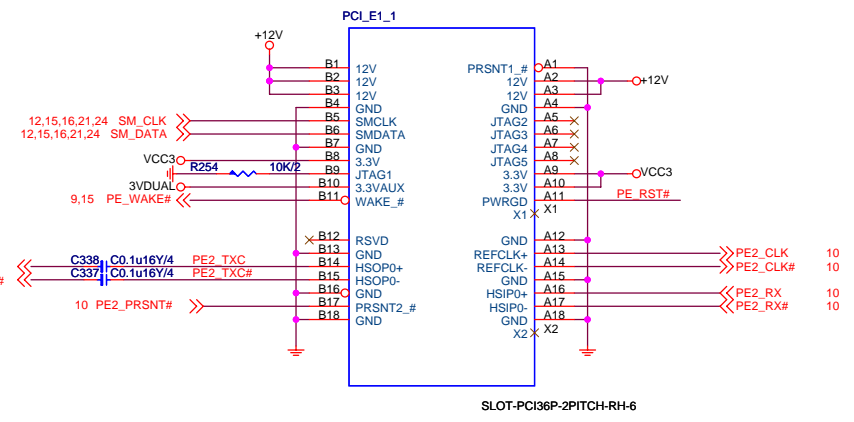
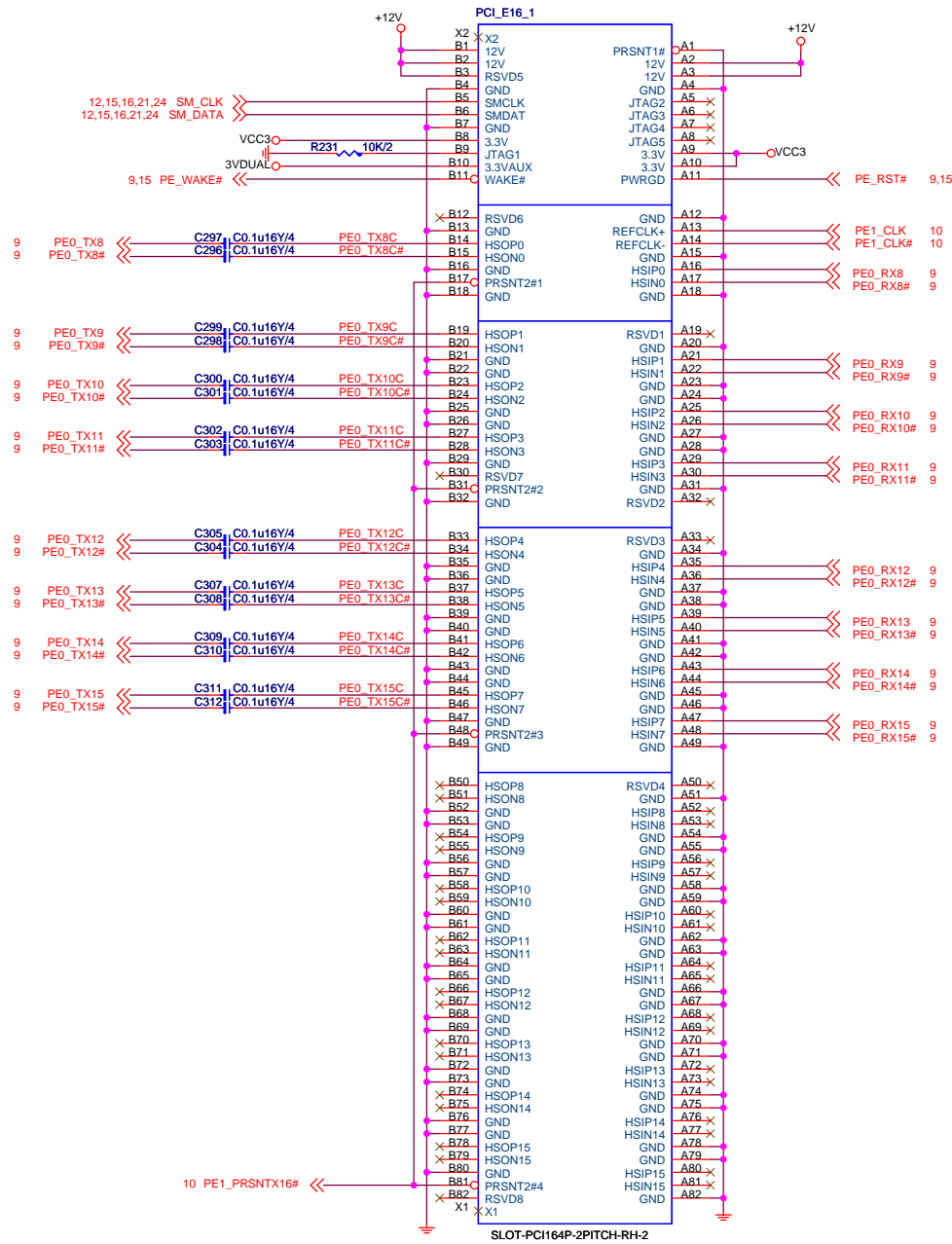


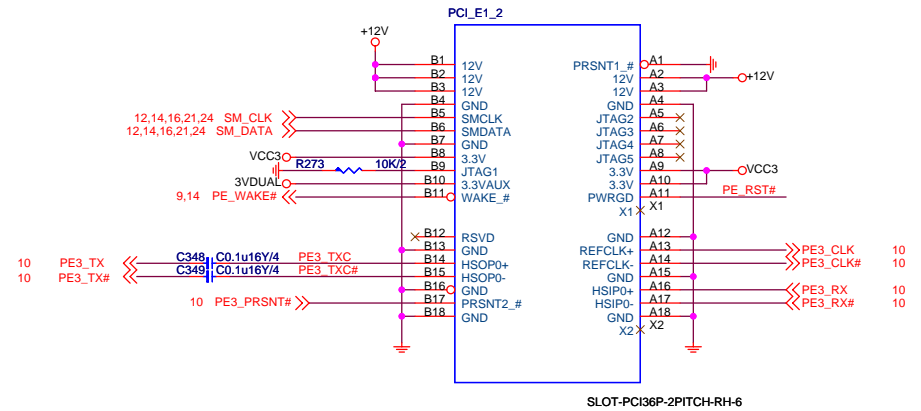
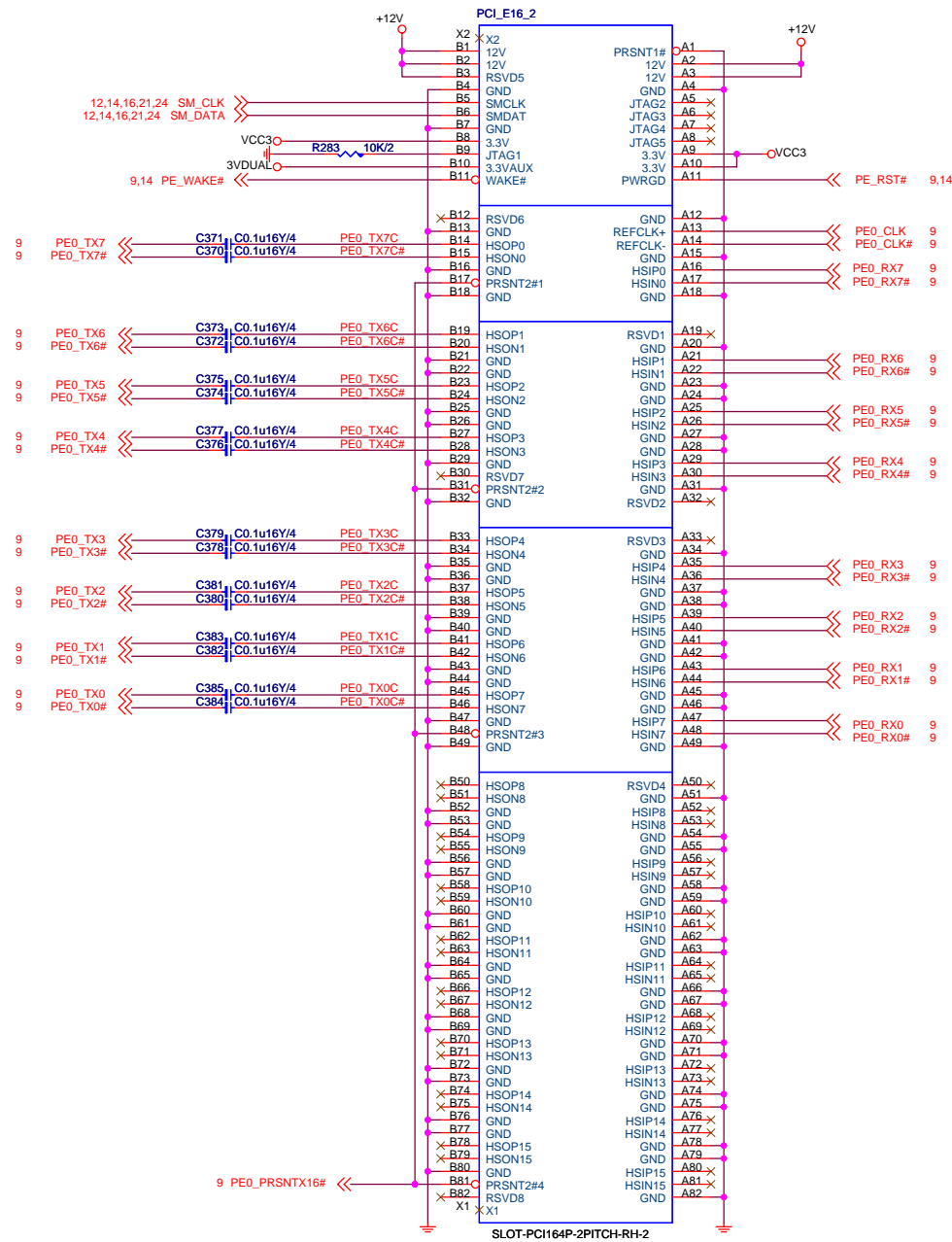






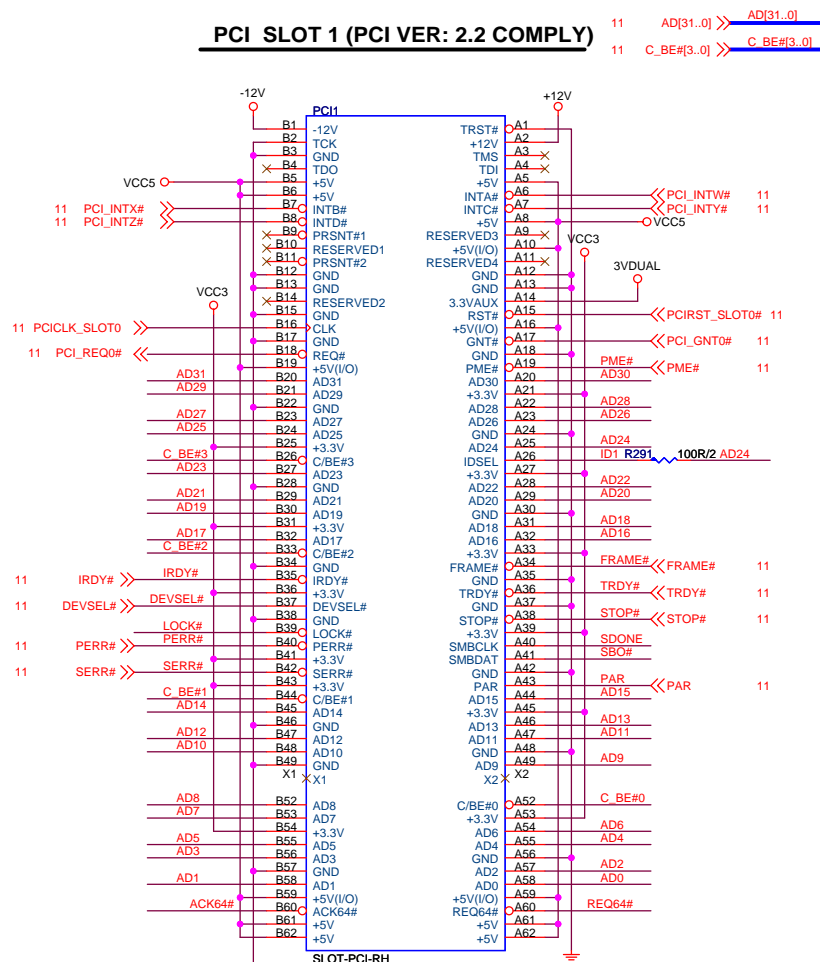
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Size: Custom	Document Number: MS-7390	Rev: 0A
Date: Friday, November 30, 2007	Sheet: 13	of 30





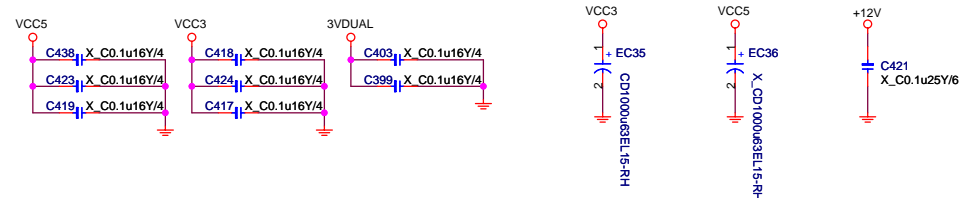
SLOT-PCI36P-2PITCH-RH-6

PCI SLOT 1 (PCI VER: 2.2 COMPLY)

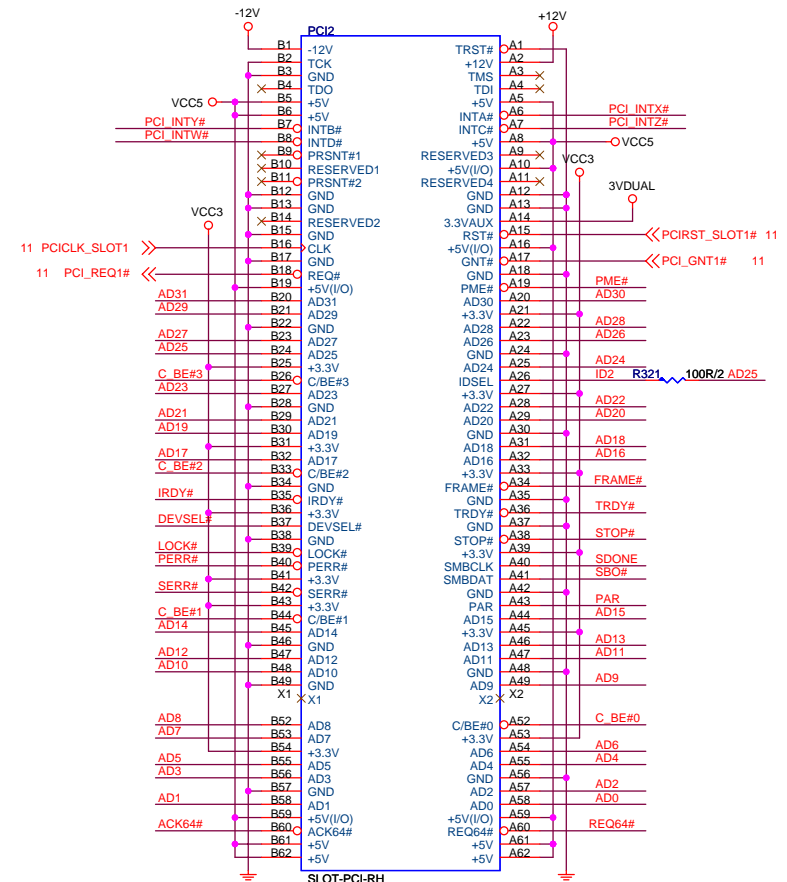


IDSEL = AD24
PCI_REQ0# PCI_GNT0#
INT W# X# Y# Z#
PCICLK_SLOT1

PCI SLOT DECOUPLING CAPACITORS



PCI SLOT 2 (PCI VER: 2.2 COMPLY)



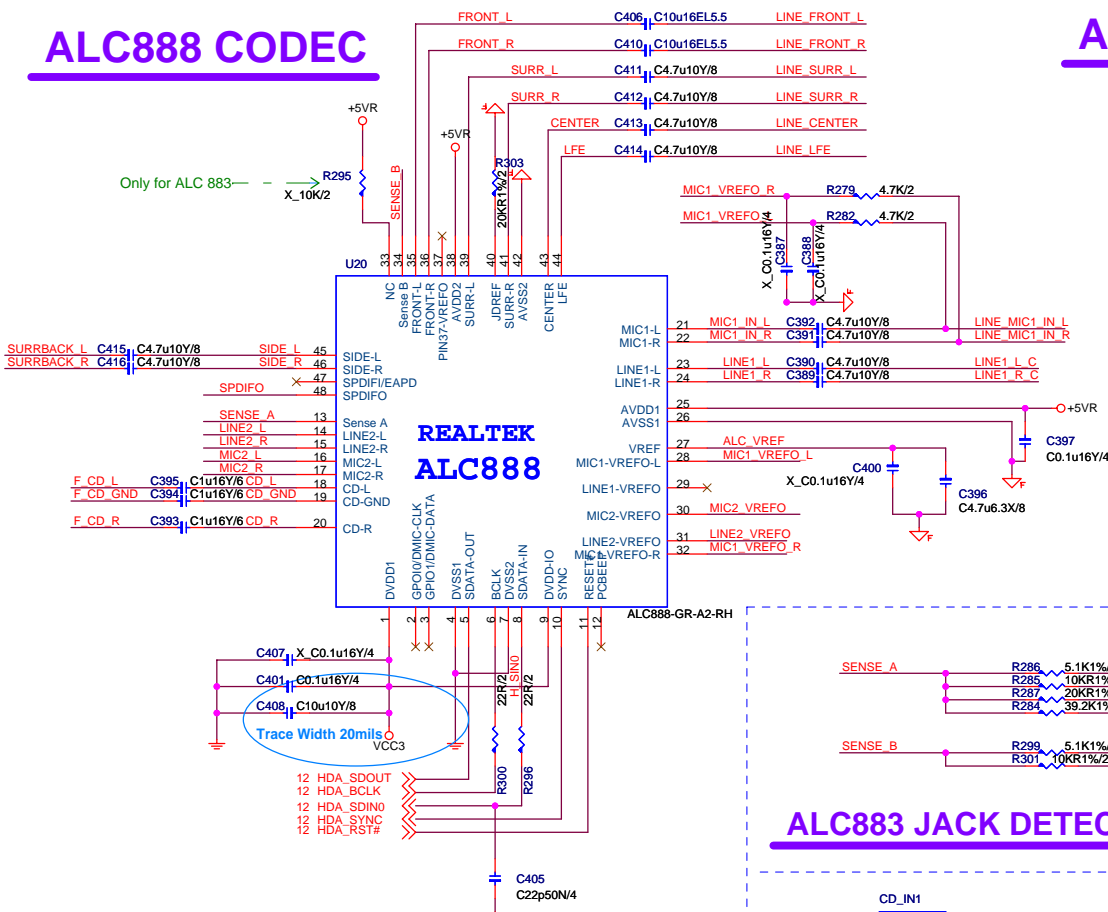
IDSEL = AD25
PCI_REQ1# PCI_GNT1#
INT X# Y# Z# W#
PCICLK_SLOT2

PCI PULL-UP / DOWN RESISTORS

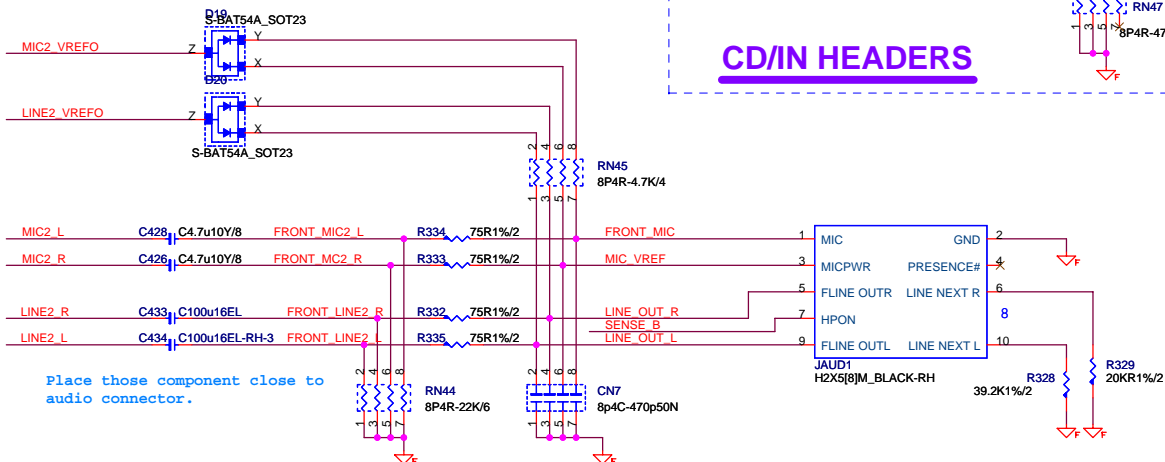


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Size	Document Number	Rev	
Custpm	MS-7390	0A	
Date:	Friday, November 30, 2007	Sheet	16 of 30

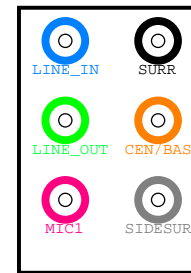
ALC888 CODEC



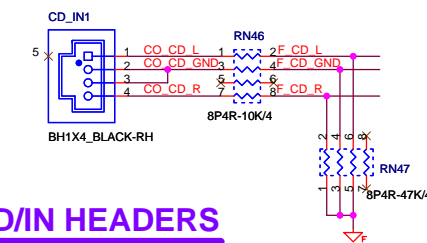
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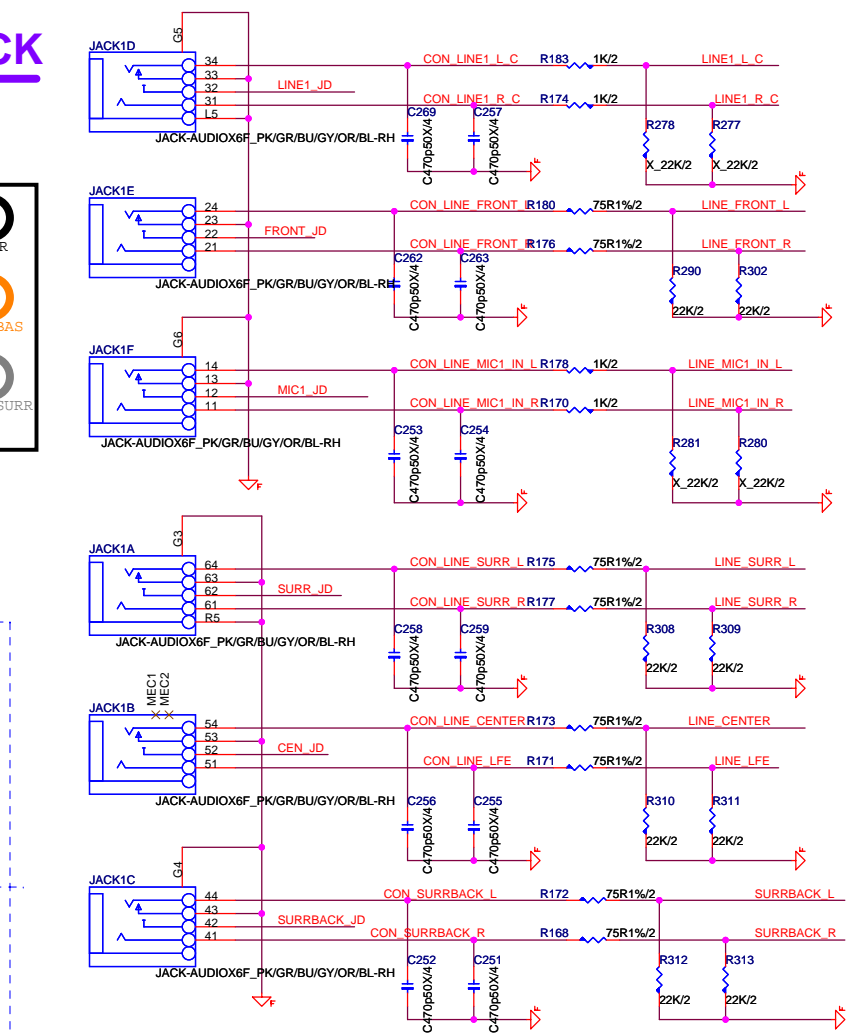
ALC888 JACK



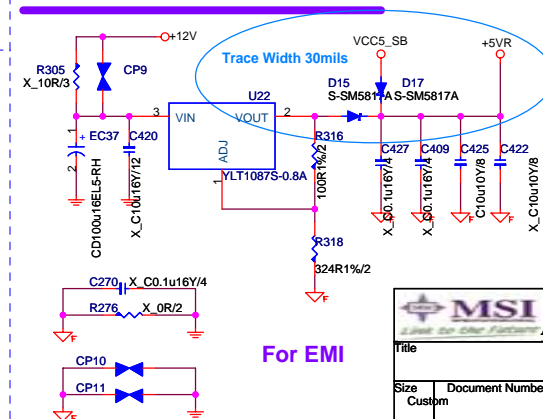
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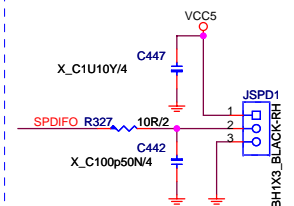
CD/IN HEADERS



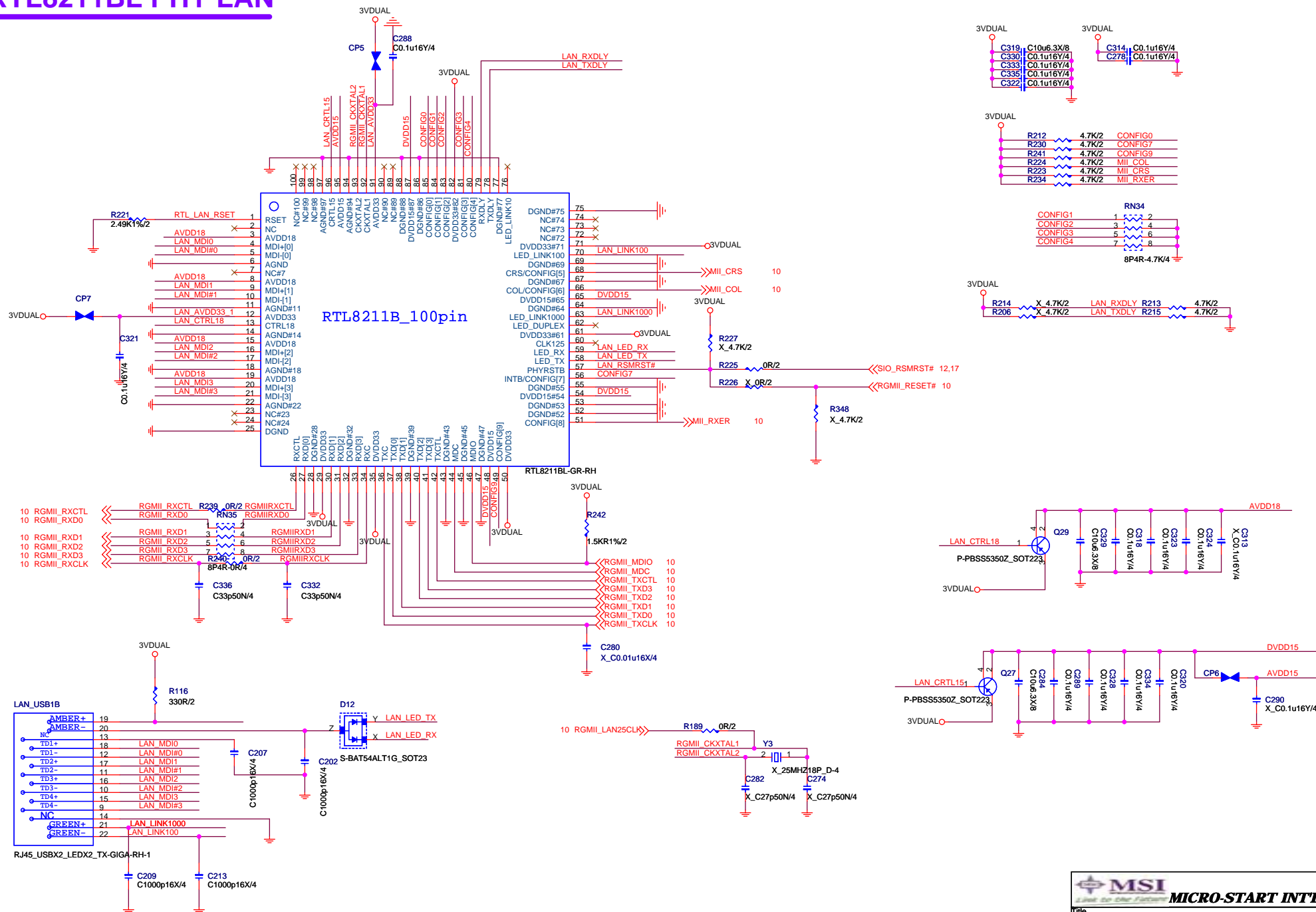
AUDIO CODE REGULATORS



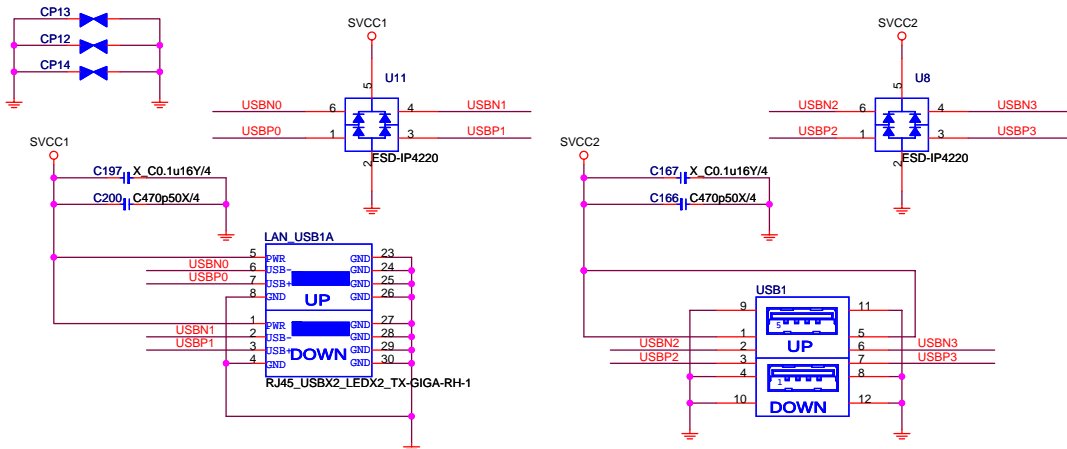
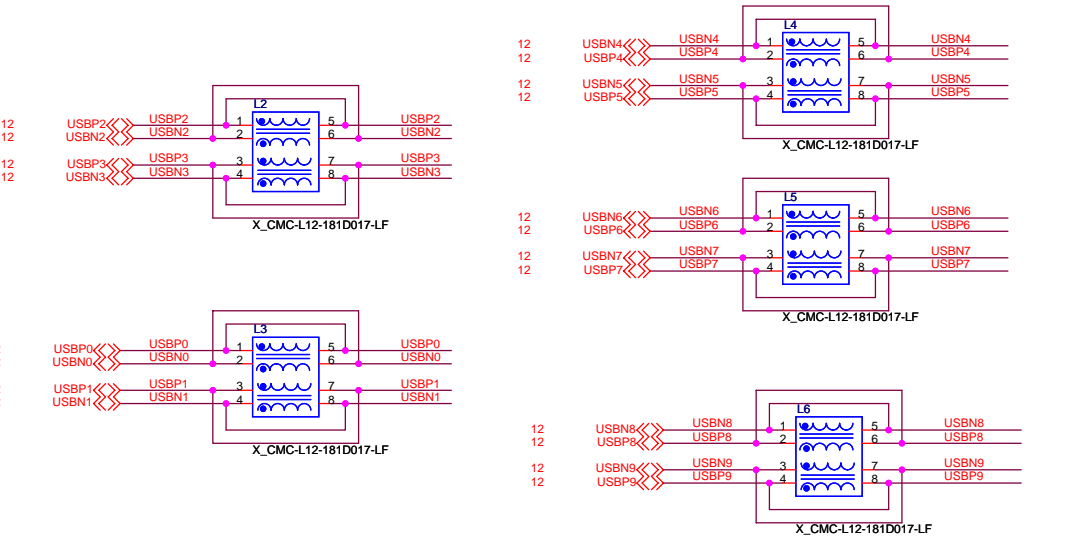
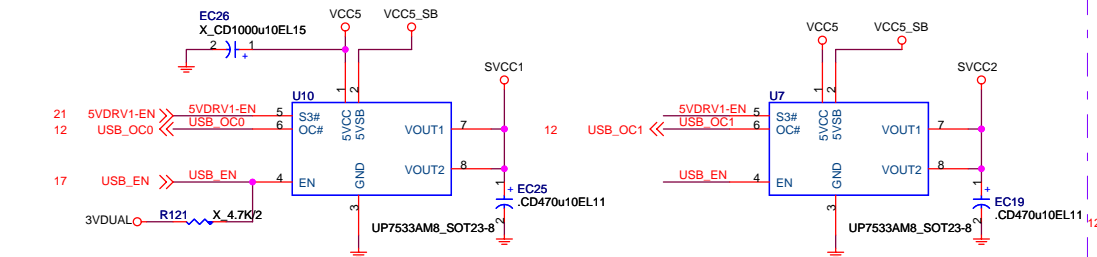
SPDIF_OUT



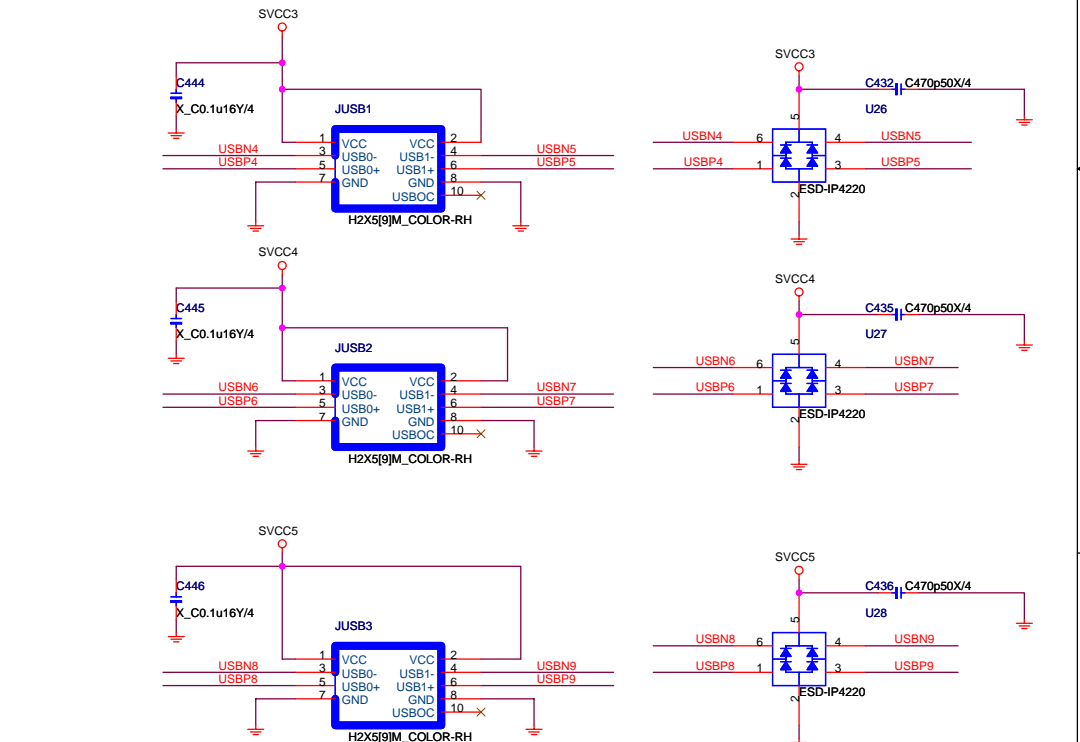
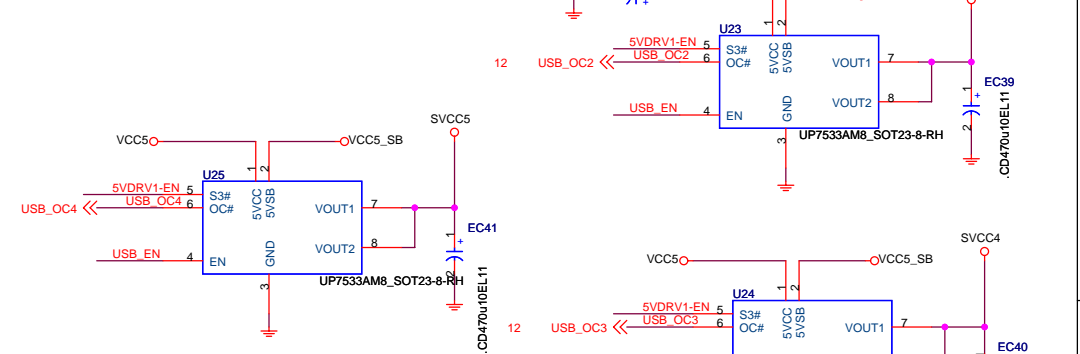
RTL8211BL PHY LAN

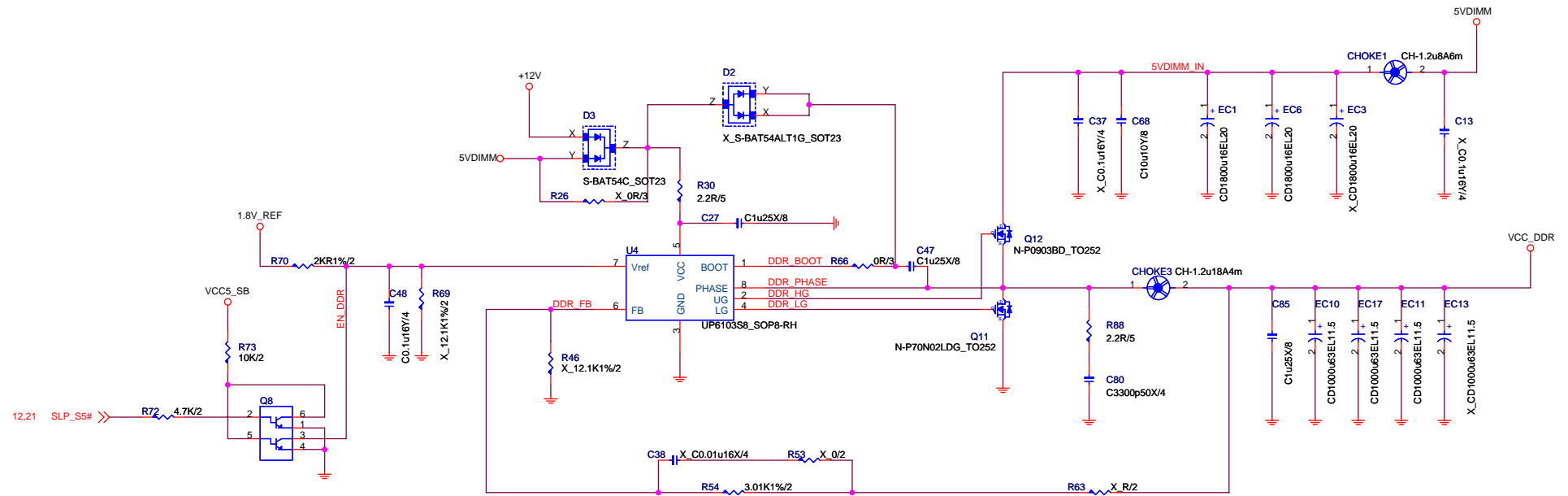


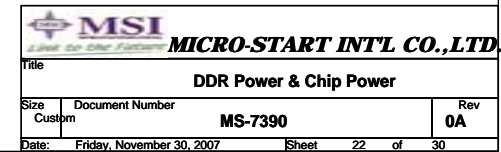
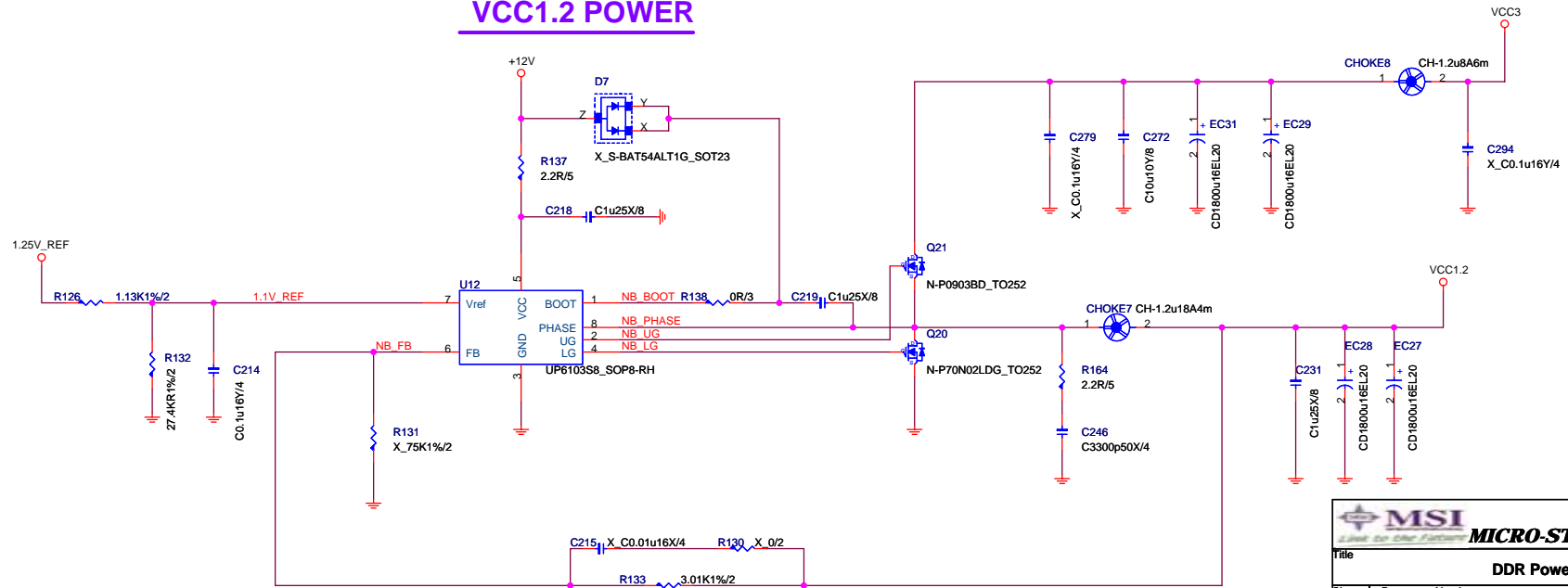
REAR USB Power

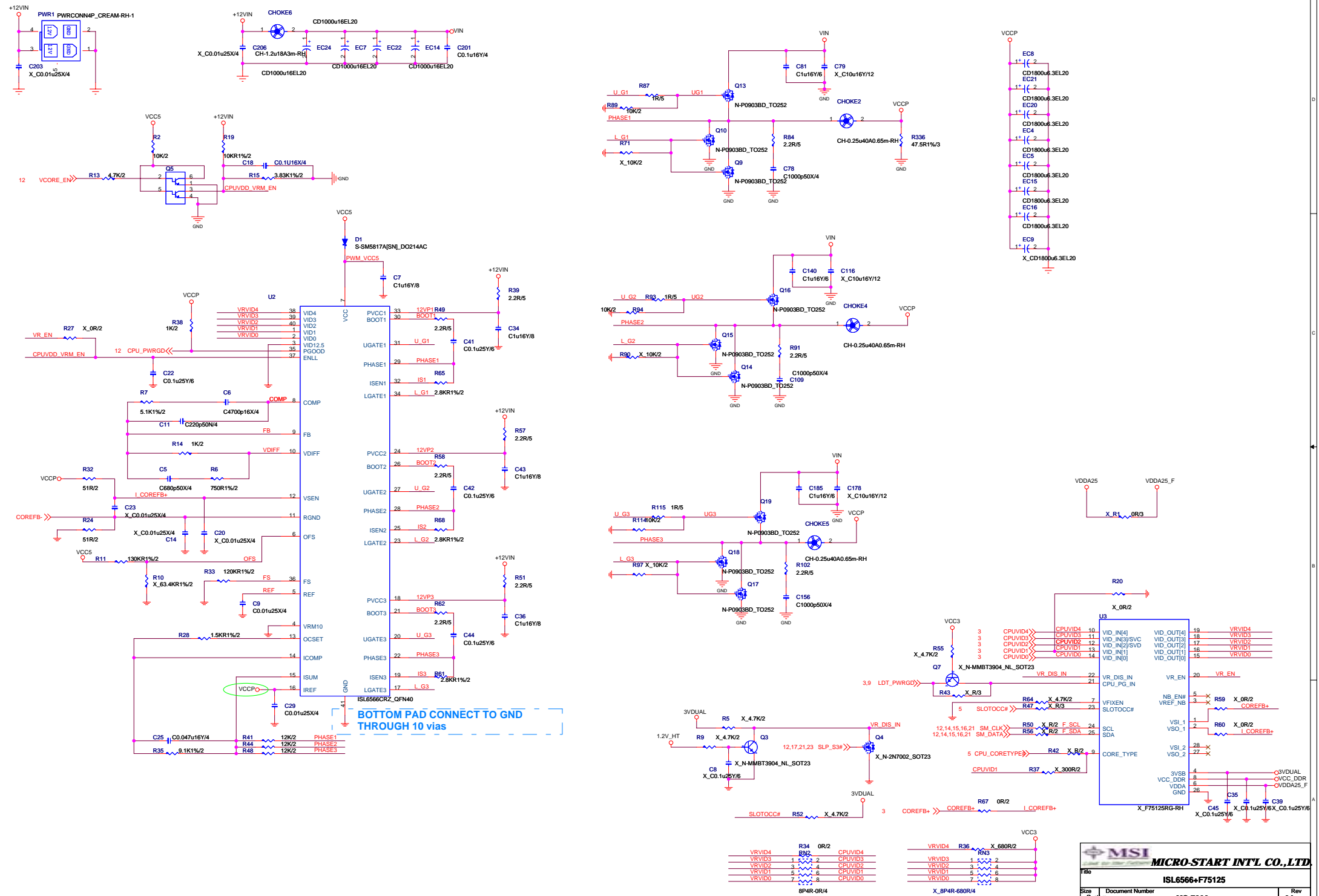


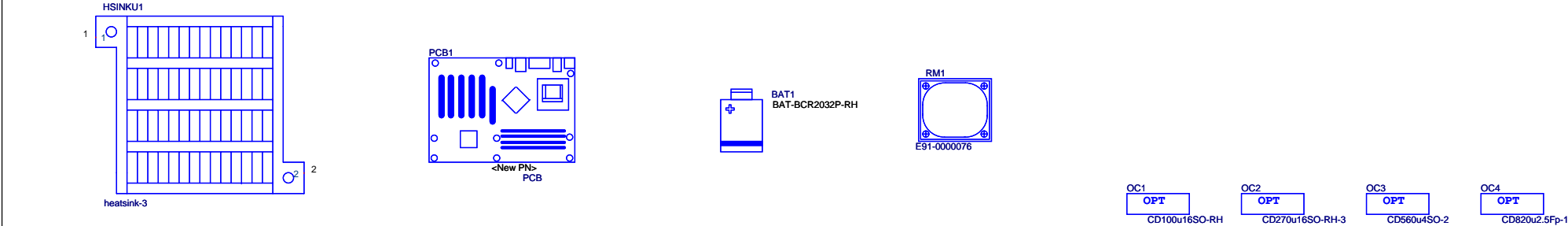
Front USB Power



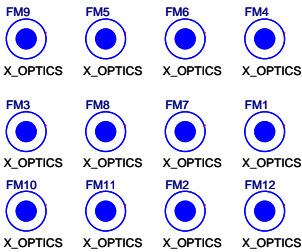




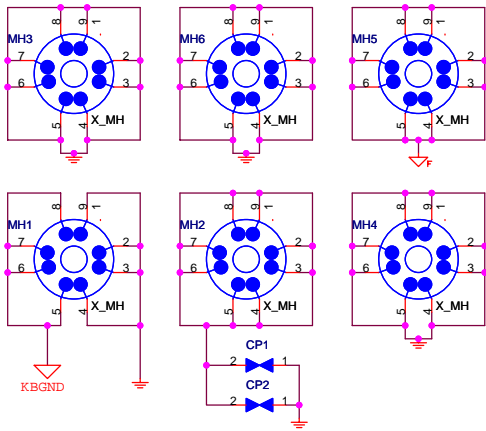




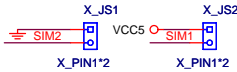
Optics Orientation Holes



Mounting Holes




Simulation




Model option table

Model type	Function	BOM Config	ERP BOM No.
MS-7390	MCP65P+RTL8211BL+ALC888+2PCI+2PCIEX8+2PCIEX1+2PS2+10USB+1COM+1Print Port+1Audio+RJ45	Cfg-7390	




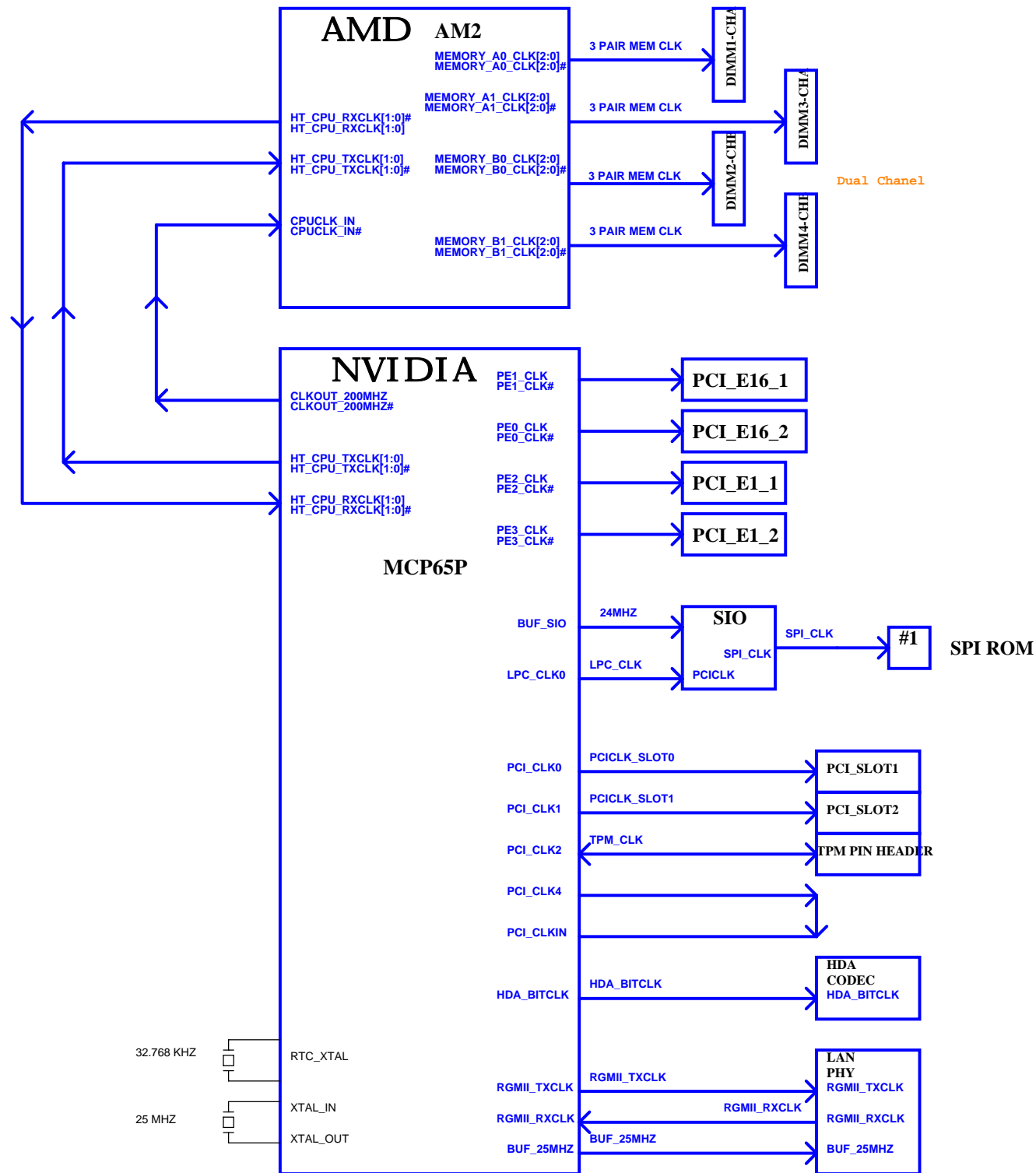
 MICRO-START INTL CO.,LTD.		
Title		
Power Map		
Size	Document Number	Rev
Custom	MS-7390	0A
Date:	Friday, November 30, 2007	Sheet 26 of 30




 MICRO-START INTL CO.,LTD.		
Title Power Sequence		
Size Custom	Document Number MS-7390	Rev 0A
Date: Friday, November 30, 2007	Sheet 27	of 30



 MICRO-START INTL CO.,LTD.		
Title		
Reset Signals		
Size	Document Number	Rev
Custom	MS-7390	0A
Date:	Friday, November 30, 2007	Sheet 28 of 30



	5	4	3	2	1
D					
C					
B					
A					



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Title		
GPIO Configuration		
Size	Document Number	Rev
Custom	MS-7390	0A
Date:	Friday, November 30, 2007	Sheet 30 of 30